

Form BTO-109 U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		ATTY. DOCKET NO. M122-1694		SERIAL NO. 09/885,393				
LIST OF ART CITED BY APPLICANT (Use several sheets if necessary)				APPLICANT Richard Holscher et al.				
				FILING DATE June 19, 2001				
U.S. PATENT DOCUMENTS								
*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate	
KAD	AA	4,755,478	07/88	Abernathy, et al.	437	41		
KAD	AB	4,870,470	09/89	Bass, Jr., et al.	357	23.5		
KAD	AC	5,312,768	05/94	Gonzalez	437	40		
KAD	AD	5,439,838	08/95	Yang	437	43		
KAD	AE	6,001,747	12/99	Annapragada, Rao V.	438	790		
	AF							
	AG							
	AH							
	AI							
	AJ							
	AK							
	AL							
FOREIGN PATENT DOCUMENTS								
		Document Number	Date	Country	Class	Subclass	Translation	
							Yes	No
	AM							
	AN							
	AO							
	AP							
	AQ							
OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, Etc.)								
KAD	AR		Wolf, et al. <i>Silicon Processing for the VLSI Era, Vol. 1 - Process Technology</i> , Lattice Press: Sunset Beach, CA, 1986, pp. 189-190.					
	AS							
	AT							
EXAMINER				DATE CONSIDERED				
K. Duda				8-12-03				
*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.								